

10525 U.S. PTO
09/490589
01/25/00

565	Subclass
257	Class

ISSUE CLASSIFICATION

PATENT NUMBER

6222248
6222248

U.S. UTILITY Patent Application

(2) <i>AD</i>	O.I.P.E.	PATENT DATE
SCANNED <i>AD</i>	O.A. <i>RC</i>	APR 24 2001

APPLICATION NO. 09/490589	CONT/PRIOR F	CLASS 257	SUBCLASS 565	ART UNIT 2814	EXAMINER <i>H. PHAM</i>
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APPLICANTS
Hoai Pham, Pham, H.

TITLE
A high voltage insulated-gate power device with integrated diode

PTO-2040
12/99

ISSUING CLASSIFICATION					
ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
257	565	257	566	570	
INTERNATIONAL CLASSIFICATION					
H 01 L	27/082				
H 01 L	27/102				
H 01 L	29/70				
H 01 L	31/11				

Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg. 4	Figs. Drwg. 9	Print Fig. 3	Total Claims 8	Print Claim for O.G. 1
The term of this patent subsequent to _____ (date) has been disclaimed.	HOAI PHAM 12/1/00 (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED <i>12-04-00</i>	
The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____	OLIK CHAUDHURI SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2800 <i>OLIK Chaudhuri 12-4-00</i> (Primary Examiner) (Date)			ISSUE FEE <i>3-6-01</i> Amount Due <i>3-6-01</i> Date Paid <i>3-6-01</i>	
The terminal _____ months of this patent have been disclaimed.	<i>J. Jones 12/00</i> (Legal Instruments Examiner) (Date)			ISSUE BATCH NUMBER <i>V92</i>	

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Formal Drawings *12/00*

ISSUE FEE IN FILE

(FACE)